550416

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date 4 November 2004 (04.11.2004)

PCT

(10) International Publication Number WO 2004/095530 A2

(51) International Patent Classification7:

H01L

(21) International Application Number:

PCT/US2004/006077

(22) International Filing Date: 17 March 2004 (17.03.2004)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data: 60/458,407

31 March 2003 (31.03.2003) US

(71) Applicant (for all designated States except US): TOKYO ELECTRON LIMITED [JP/JP]; TBS Broadcast Center, 3-6 Akasaka 5-Chrome, Minato-Ku, Tokyo 107-8481 (JP).

(72) Inventors; and

- (75) Inventors/Applicants (for US only): ESCHER, Gary [US/US]; 29 Gallant Drive, Hampstead, NH 03841 (US). ALLEN, Mark, A. [US/US]; 18 Winthrop Street, Essex, MA 01929 (US). KUDO, Yasuhisa [JP/JP]; c/o Tokyo Electron limited, 3-6, Akasaka 5-chome, Minato-ku, Tokyo 107-8481 (JP).
- (74) Agent: SACHAR, Surinder; Oblon, Spivak, McClelland, Maier & Neustadt, P.C., 1940 Ducke Street, Alexandria, VA 22314 (US).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: A METHOD FOR ADJOINING ADJACENT COATINGS ON A PROCESSING ELEMENT

(57) Abstract: Two or more coatings applied to processing elements of a plasma processing system are treated with protective barriers or coatings. A method is described for adjoining two or more coatings on the processing element. Having applied a first protective barrier, a portion of the first protective barrier is treated. A second protective barrier is then applied over at least a portion of a region to which the first protective barrier was applied.

